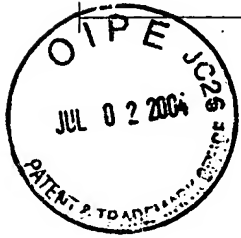


IFW

PARSONS HSUE & DE RUNTZ LLP



655 MONTGOMERY STREET, SUITE 1800 SAN FRANCISCO, CA 94111
TEL 415.318.1160 FAX 415.693.0194

June 30, 2004

Mail Stop Amendment
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Applicant(s): Zhang et al.
Title: MOSFET Modeling for IC Design Accurate for High Frequencies
Application No.: 10/616,765 Filing Date: July 9, 2003
Examiner: Rossoshek, Yelena Group Art Unit: 2825
Docket No.: BTAT.001US1 Conf. No.: 2284

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) Return Receipt Postcard;
- (2) This Transmittal Letter (in duplicate – 1 page); and
- (3) Amendment (8 pages).

- ☒ No additional fee is required.
☐ The fee has been calculated as shown below:

CLAIMS AS AMENDED

	Claims Remaining <u>After Amendment</u>		Highest No. Previously <u>Paid For</u>		Present <u>Extra</u>	<u>Rate</u>	Additional <u>Fee</u>	
Total Claims	23	Minus	28	=	0	x \$18.00	\$	0.00
Independent Claims	3	Minus	6	=	0	x \$86.00	\$	0.00
<input type="checkbox"/>	Fee of _____ for the first filing of one or more multiple dependent claims per application						\$	
<input type="checkbox"/>	Fee for Petition for Extension of Time						\$	
Total additional fee for this Amendment:							\$	0.00

- ☒ Conditional Petition for Extension of Time: If an extension of time is required for timely filing of the enclosed document(s) after all papers filed with this transmittal have been considered, an extension of time is hereby requested.
- ☒ Please charge any additional fees required and credit any overpayment to our Deposit Account No. 502664.

Certificate of Mailing Under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on 6/30/04

Eileen Bowe
Signature

Respectfully submitted,

Michael G. Cleveland
Michael G. Cleveland
Reg. No. 46,030



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Xisheng Zhang et al.
Assignee: Cadence Design Systems, Inc.
Title: MOSFET Modeling for IC Design Accurate for High Frequencies
Application No.: 10/616,765 Filing Date: July 9, 2003
Examiner: Rossoshek, Yelena Group Art Unit: 2825
Docket No.: BTAT.001US1 Conf. No.: 2284

Certificate of Mailing Under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on 10-30-04

Signature

Gilbert Bower

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

In response to the Office Action of March 31, 2004, Applicant responds as follows:

Amendments to the Claims are reflected in the listing of the claims, which begins on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.

BTAT.001US1

Serial No.: 10/616,765